

Electronic Acknowledgement Receipt

EFS ID:	4981547
Application Number:	10534962
International Application Number:	
Confirmation Number:	1696
Title of Invention:	Circuit board, multi-layer wiring boards, method of producing circuit boards and method of producing multilayer wiring boards
First Named Inventor/Applicant Name:	Masayoshi Kondo
Customer Number:	25461
Filer:	Robert G. Weilacher/Bess Murray
Filer Authorized By:	Robert G. Weilacher
Attorney Docket Number:	033036.088
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Application Type:	U.S. National Stage under 35 USC 371

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed (58/08)	088ids.PDF	61056 <small>8514a70765d984ae538053e33c2947c5a04 b0def</small>	no	3

Warnings:

Information:

2	Foreign Reference	088IDSref.PDF	1023320	no	25
			f69a6cd5f78f1921763b132441d4c40f072a5e4		

Warnings:**Information:****Total Files Size (in bytes):**

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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